

9 20. The phase locked loop according to claim ~~18~~<sup>7</sup>, wherein said phase comparison circuit contains an exclusive-OR gate.

AS  
and  
10 21. The phase locked loop according to claim ~~18~~<sup>7</sup>, which comprises a current mirror circuit connected to said phase comparison circuit for stabilizing a current flowing into said phase comparison circuit and into said second driver stage.--

Remarks:

The preliminary amendment is being filed in an effort to present an application in proper U.S. format and to present claims in proper U.S. claim idiom for examination.

The newly entered claims are fully supported in the original claims.

An early action on the merits of the claims is requested.

Respectfully submitted,



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